

# Product Change Notification



## Change Notification # 2012020602B

### BIOS Update for CECx-10

Initial Release Date: 2012 February 6  
Last Revision Date: 2014 March 13  
Change Type: BIOS/Firmware Change  
Affected Product Family: CEC (Compact Embedded Computers)  
Reason for Change: BIOS Update to V1.11  
Anticipated impact on Form, Fit and Function: Minor impact anticipated  
Anticipated impact on Quality or Reliability: Improved Quality and Reliability anticipated  
Customer contact: [info@mpl.ch](mailto:info@mpl.ch)

### Detailed Description of Change

The affected products receive a new BIOS release (V1.11). See the 'BIOS Release Notes' section below for a detailed overview of the changes.

### Affected Products List

<i>Sales Part#</i>	<i>MPL Manufacturing Part#</i>	<i>MPL Component Stock#</i>	<i>Replacement Part#</i>
CEC6-10	MED-10414-004 [A]	-	-
CEC7-10	MED-10414-002 [A, B, C, D]	-	-

### PCN Revision History

<i>Revision</i>	<i>Date</i>	<i>Description</i>
A	2012-02-06	Initial Release
B	2014-03-13	Reflect BIOS V1.11

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Written	Date 06/02/12	Visa MW	PCN # <b>2012020602B</b>	Rev. B

# Product Change Notification

## BIOS Release Notes

**BIOS Version:** 1.11

**Information:**

- **Build date:** 03/12/14 (mm/dd/yy)
- **BIOS No:** MEV-10121-001
- **Core BIOS:** Phoenix® Embedded BIOS® w/StrongFrame® Technology, V6.907
- **VGA BIOS:** Intel® SCH Chipset VGA BIOS Build 0017
- **PXE Boot ROM:** Intel® Boot Agent GE v1.3.72

**Features & Fixes:**

- Improved detection of SSD drives

**BIOS Version:** 1.10

**Information:**

- **Build date:** 02/06/12 (mm/dd/yy)
- **BIOS No:** MEV-10121-004
- **Core BIOS:** Phoenix® Embedded BIOS® w/StrongFrame® Technology, V6.907
- **VGA BIOS:** Intel® SCH Chipset VGA BIOS Build 0017
- **PXE Boot ROM:** Intel® Boot Agent GE v1.3.72

**Features & Fixes:**

- SATA drive handling reworked (Option ROM removed, configurable mode (IDE/AHCI), configurable port mapping)
- Use TSEG memory region for SMM
- HPET support added
- MCFG ACPI table added
- SMBIOS information expanded
- Cleanup for BIOS setup
- Added support for CPU power state C2
- Bugfix PNP protected mode entrypoint
- Bugfix ATA geometry calculation
- Other enhancements

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	Date	Visa
Written	06/02/12	MW

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