

Product Change Notification



Change Notification # 2009121001B

BIOS Update for MIP8 / MIP9 / MIP10 / MIP11

Initial Release Date: 2009 December 10
Last Revision Date: 2010 May 7
Change Type: BIOS/Firmware Change
Affected Product Family: MIP (MPL Industrial PC)
Reason for Change: BIOS Update to V1.52
Anticipated impact on Form, Fit and Function: No impact anticipated
Anticipated impact on Quality or Reliability: Improved Quality and Reliability anticipated
Customer contact: info@mpl.ch

Affected Products List

Sales Part#	MPL Manufacturing Part#	MPL Component Stock#	Replacement Part#
MIP8-1	MED-10363-081 Rev. A and later	-	-
	MED-10363-002 Rev. D and later		
MIP9-1	MED-10363-091 Rev. A and later	-	-
	MED-10363-002 Rev. D and later		
MIP10-1	MED-10363-001 Rev. D and later	-	-
	MED-10363-002 Rev. D and later		
MIP11-1	MED-10363-111 Rev. C and later	-	-
	MED-10363-002 Rev. D and later		

Detailed Description of Change

The affected products receive a new BIOS release (V1.52). See the 'BIOS Release Notes' section below for a detailed overview of the changes.

PCN Revision History

Revision	Date	Description
A	2009-12-11	Initial Release
B	2010-05-07	Reflect BIOS V1.52

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Written	Date 10/12/09	Visa MW	PCN # 2009121001B	Rev. B

Product Change Notification



BIOS Release Notes

BIOS Version: 1.52

Information:

- **Build date:** 05/07/10 (mm/dd/yy)
- **BIOS No:** MEV-10113-201
- **Core BIOS:** General Software Embedded BIOS® 2000, V5.3.3
- **VGA BIOS:** PC14.4 build 1270
- **PXE Boot ROM:** Intel® Boot Agent v1.2.70

Features & Fixes:

- Fixed issue where the BIOS would hang if ACPI is disabled
- Allocation of I/O addresses for PCI devices up to 0xEFFF

BIOS Version: 1.51

Information:

- **Build date:** 12/10/09 (mm/dd/yy)
- **BIOS No:** MEV-10113-201
- **Core BIOS:** General Software Embedded BIOS® 2000, V5.3.3
- **VGA BIOS:** PC14.4 build 1270
- **PXE Boot ROM:** Intel® Boot Agent v1.2.70

Features & Fixes:

- Fixed issue where 1.8GHz CPU would run at 1.4GHz in a non-ACPI operating system
- Fixed CPU speed display at POST
- Added PCI reset for PCI-to-PCI bridges on PC104+ bus

BIOS Version: 1.50

Information:

- **Build date:** 08/07/09 (mm/dd/yy)
- **BIOS No:** MEV-10113-201
- **Core BIOS:** General Software Embedded BIOS® 2000, V5.3.3
- **VGA BIOS:** PC14.4 build 1270
- **PXE Boot ROM:** Intel® Boot Agent v1.2.70

Features & Fixes:

- Initial BIOS release for MIP family with PCB Rev. C and later
- Suitable for MIP8, MIP9, MIP10, MIP11

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	10/12/09	MW			